

U.S. **UTILITY** Patent Application

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PATENT DATE

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APPLICATION NO. 09/684868	CONT/PRIOR	CLASS 365 716	SUBCLASS 17	ART UNIT 2818	EXAMINER D.L.
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Diagnostic architecture using FPGA core in system on a chip design

PTO-2040
12/99

ISSUING CLASSIFICATION								
ORIGINAL				CROSS REFERENCE(S)				
CLASS	SUBCLASS			CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)			

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	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
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<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner)		ISSUE FEE	
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